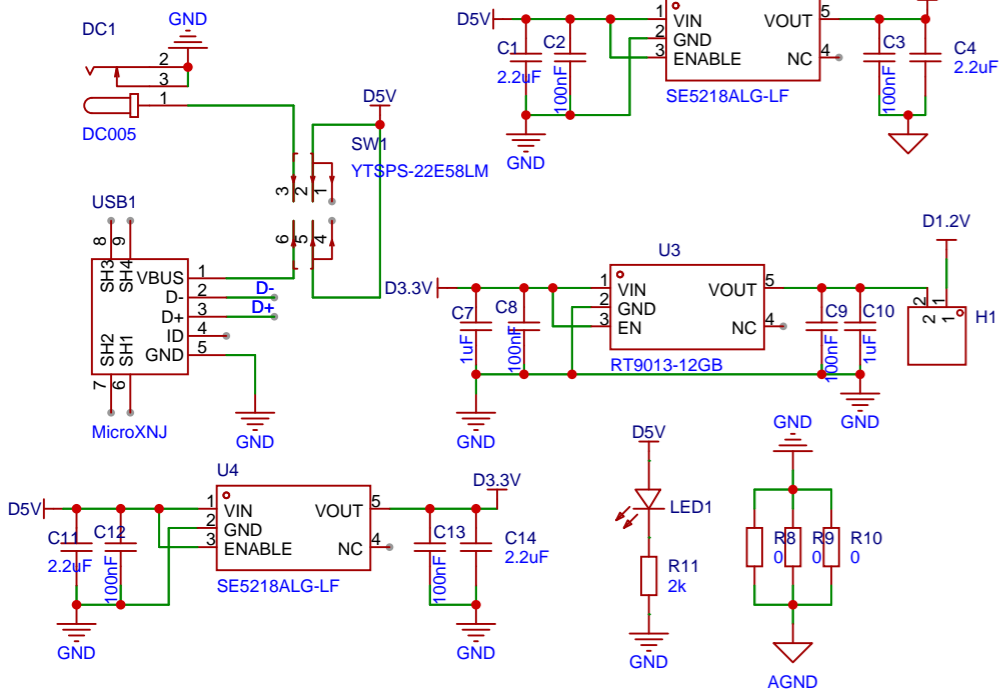
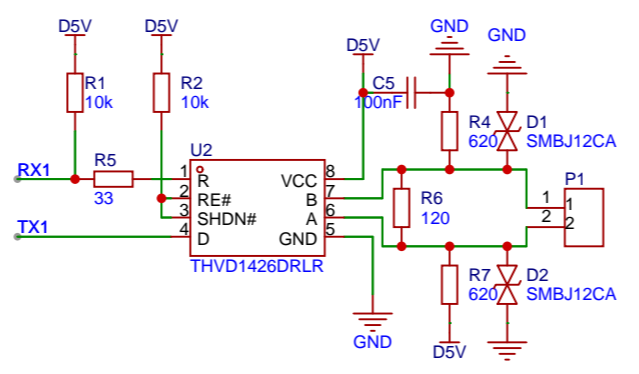


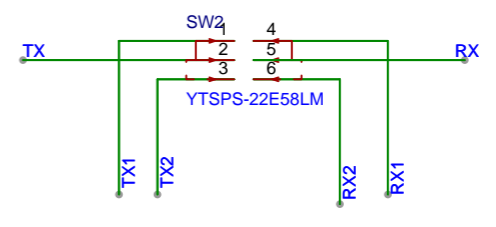
电源



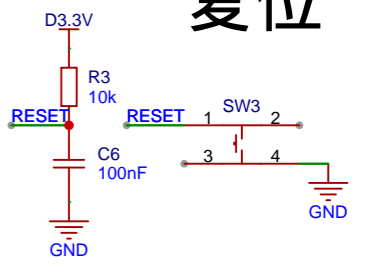
RS485模块



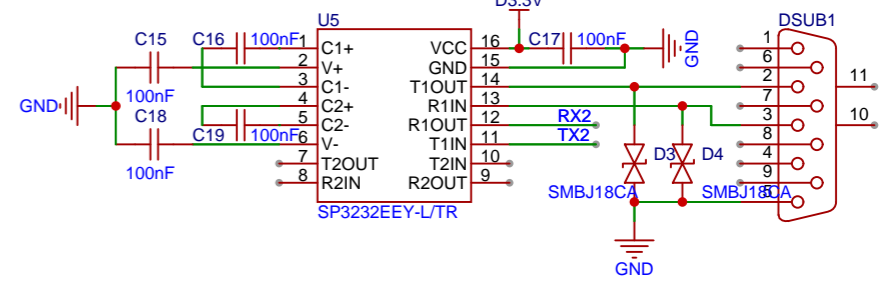
ON:RS232 OFF:RS485



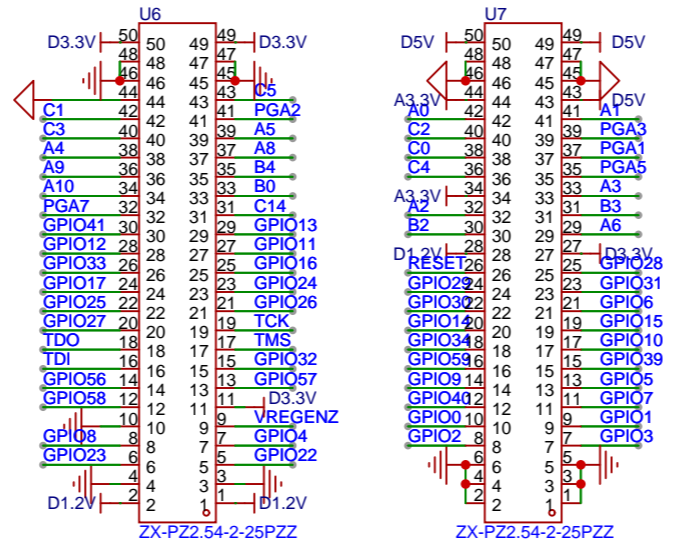
复位



RS232模块

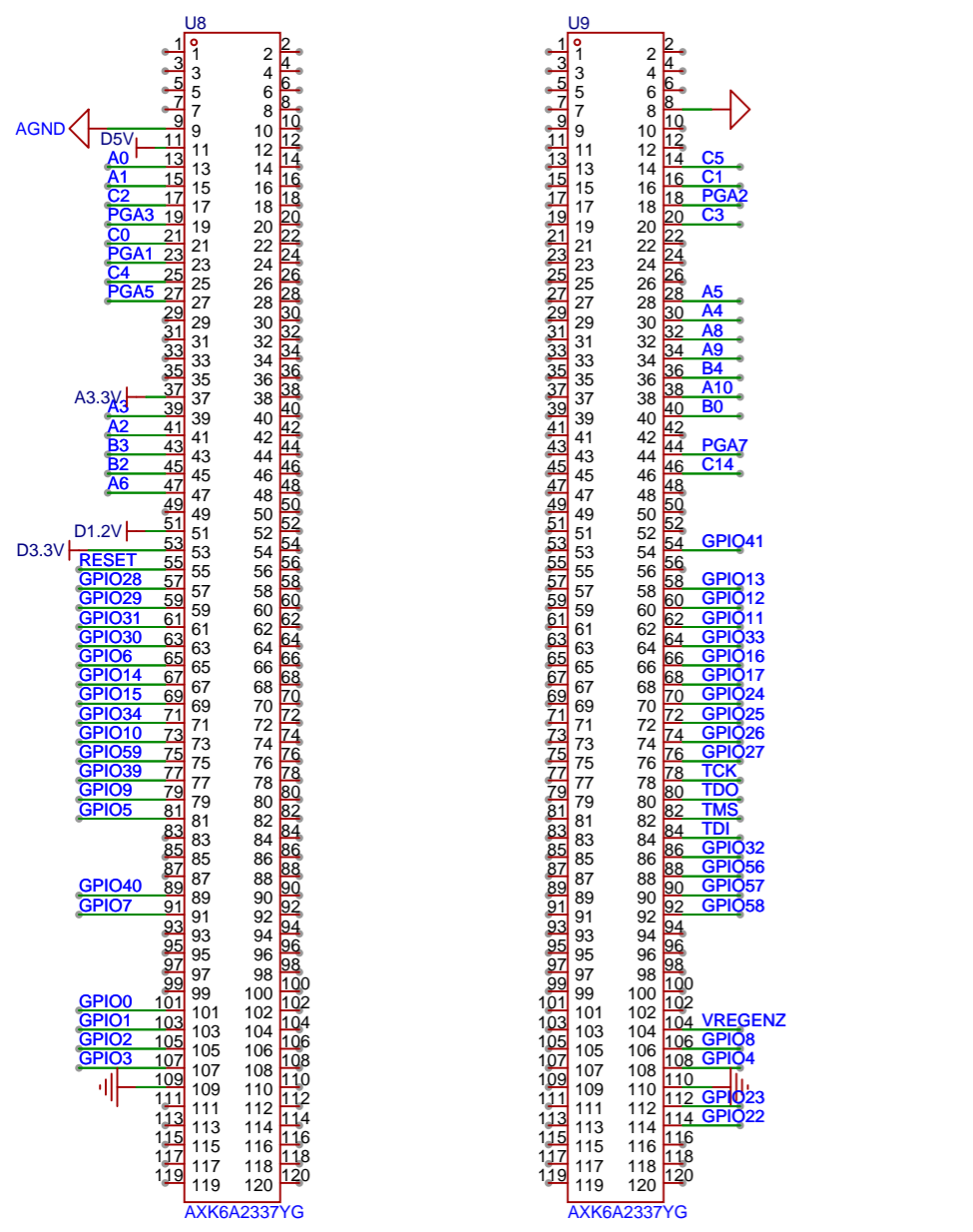


外扩接口

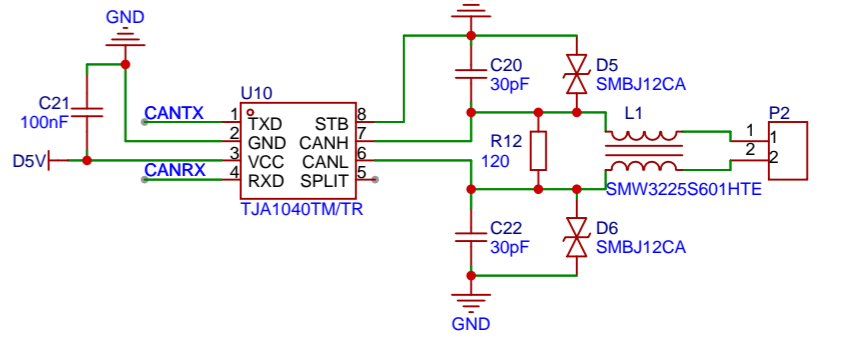


如果使用外部降压芯片为内核供电，VREGENZ拉高至3.3V
如果使用芯片内部LDO为内核供电，VREGENZ接地

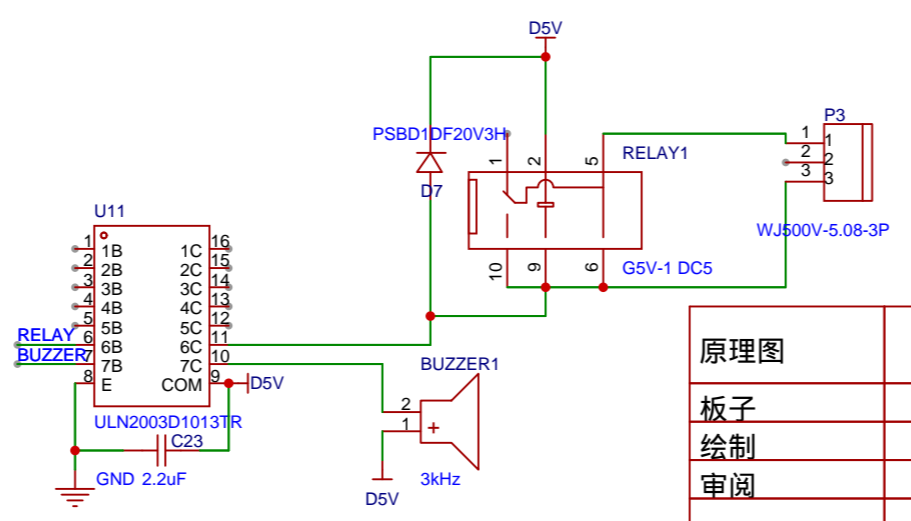
280049核心模块



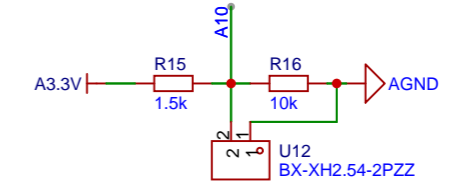
CAN模块



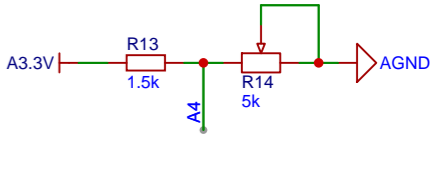
蜂鸣器，继电器驱动模块



温度采集模块

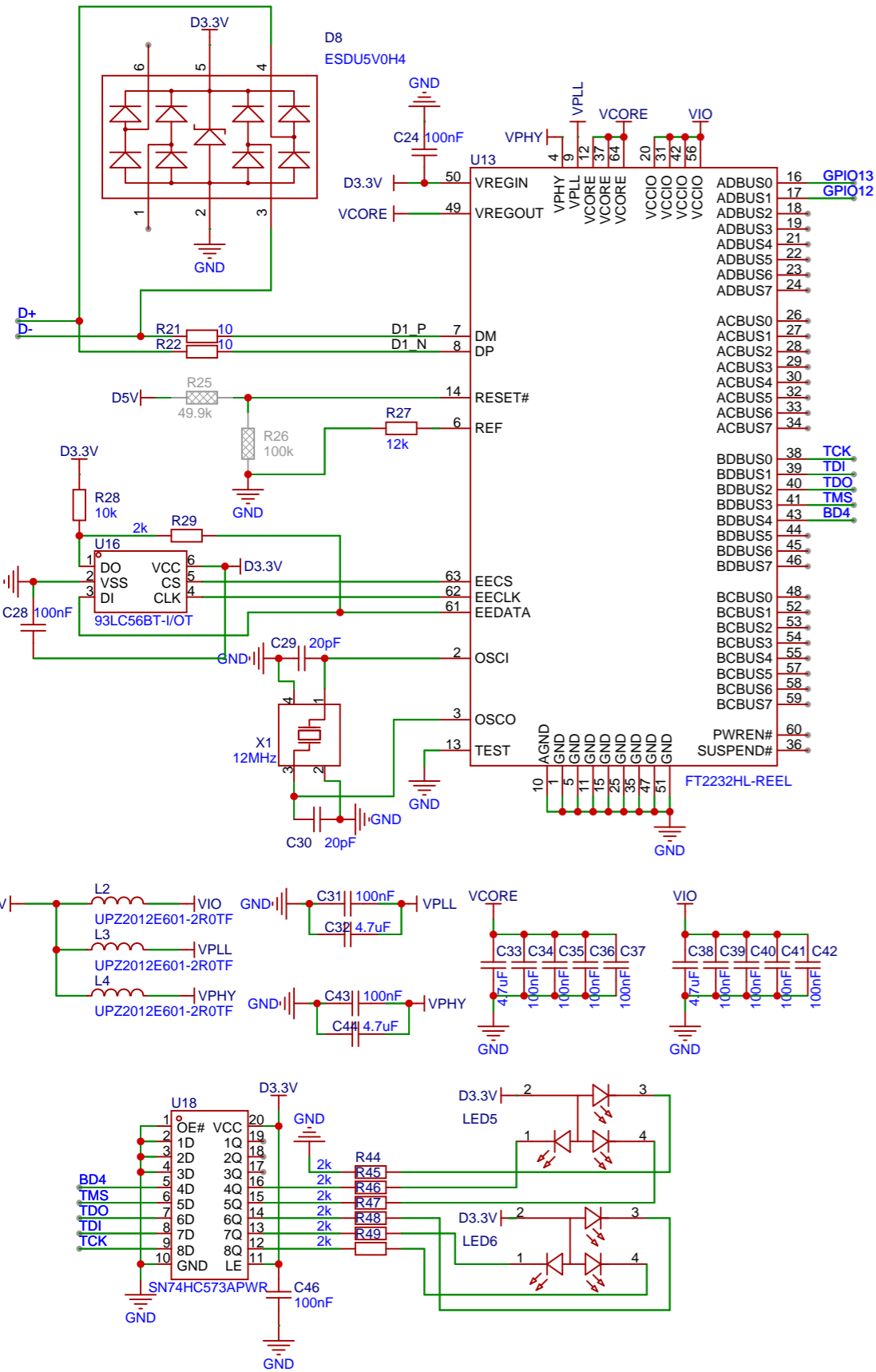


ADC模块

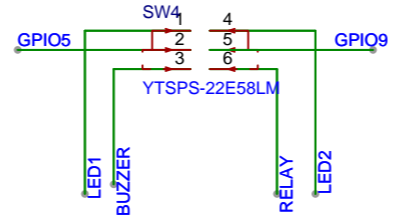


原理图	开发板底板		创建日期	2025-04-21
板子	049开发板		更新日期	2025-07-01
绘制			图页	P1
审阅			QXS320F280049开发评估板(0515)	
		版本		
		尺寸	A4	页 1 共 2
嘉立创EDA		嘉立创EDA		

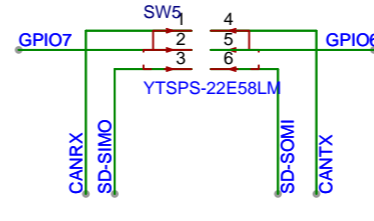
JTAG模块



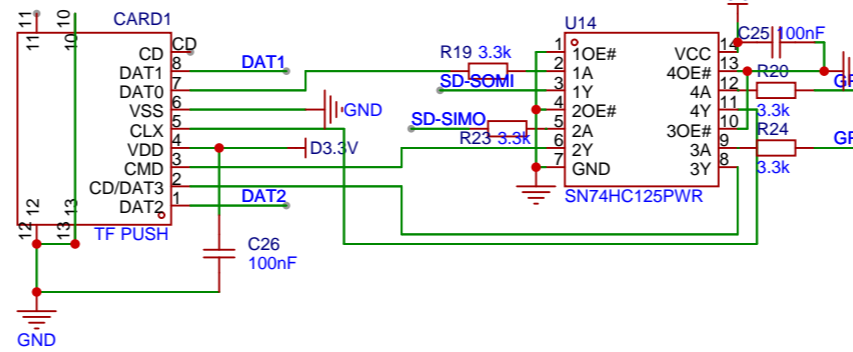
ON:BUZ
OFF:LED,R/D



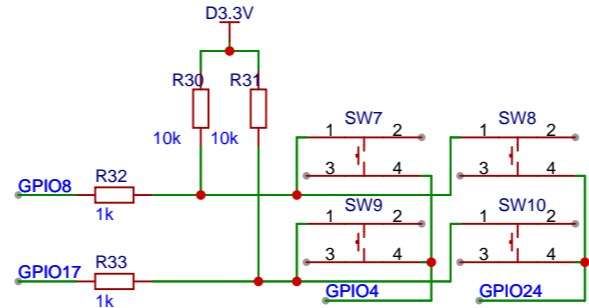
ON:SD卡
OFF:CAN



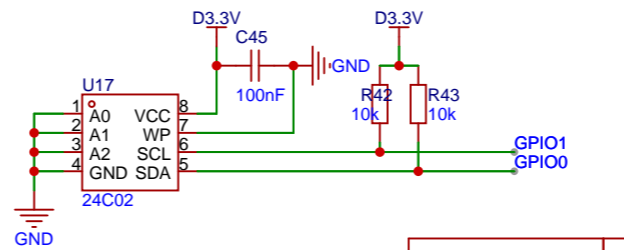
SD card



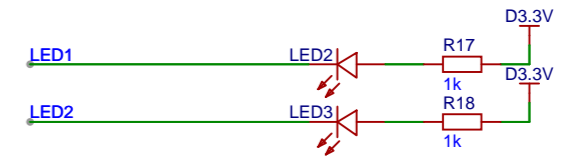
矩阵按键模块



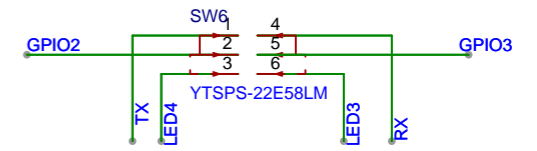
EEPROM模块



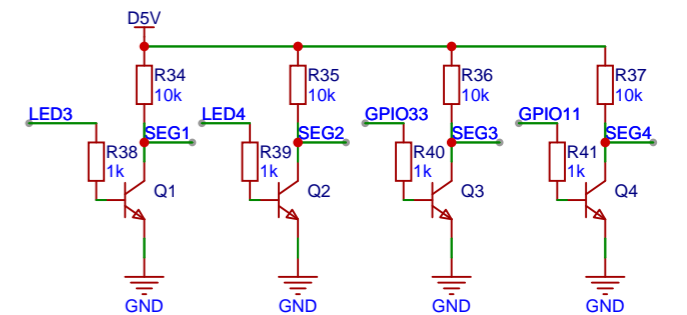
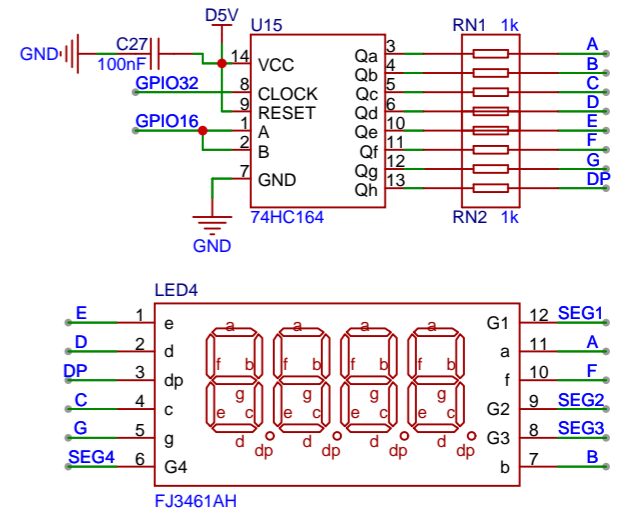
LED模块



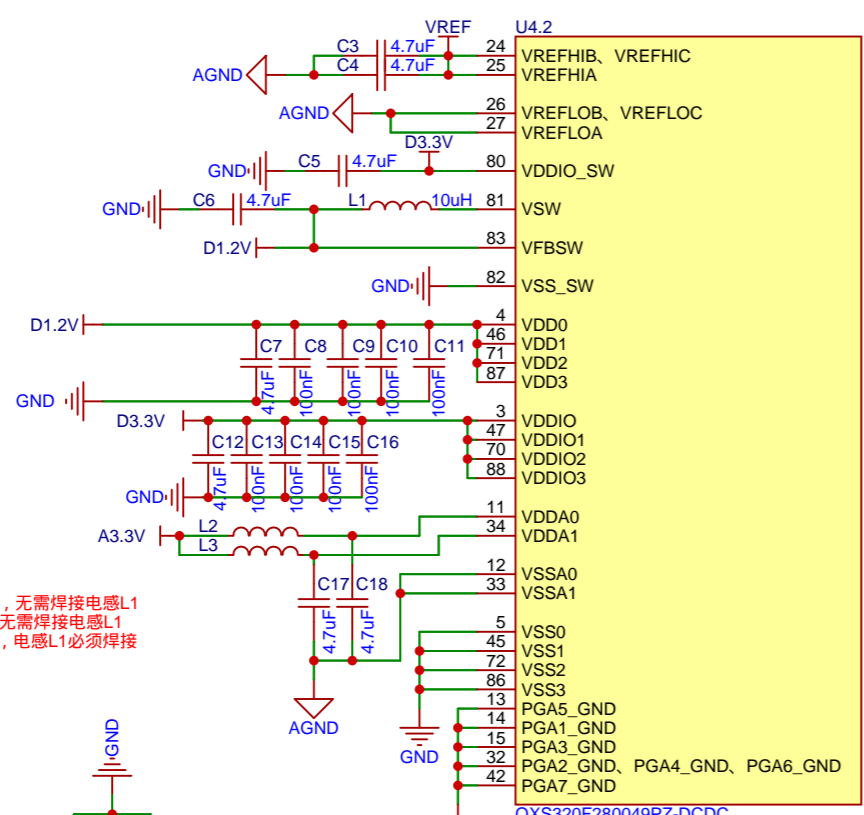
ON:数码管
OFF:通讯



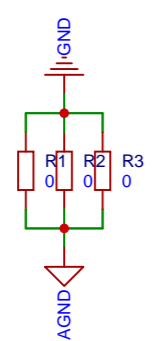
数码管显示模块



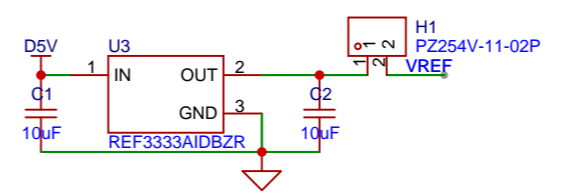
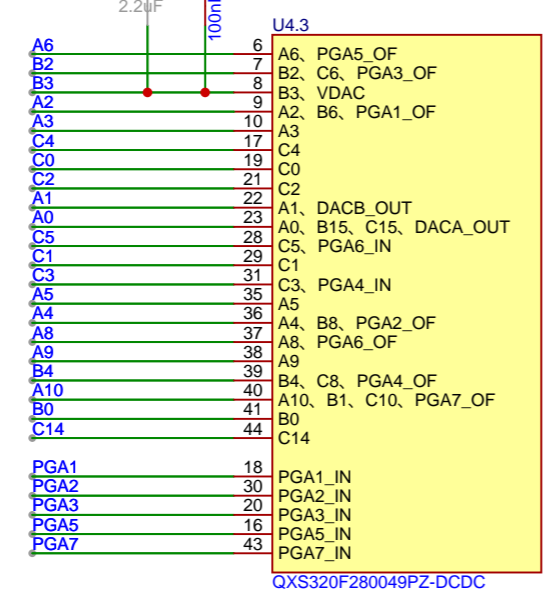
原理图	开发板底板		创建日期	2025-04-22
板子	049开发板		更新日期	2025-06-09
绘制			图页	P2
审阅	QXS320F280049开发评估板(0515)			
	版本	尺寸	页 2 共 2	
嘉立创EDA		V1.0	A4	嘉立创EDA



如果使用外部降压转换器为内核供电，无需焊接电感L1
 如果使用芯片内部LDO为内核供电，无需焊接电感L1
 如果使用芯片内部DCDC为内核供电，电感L1必须焊接

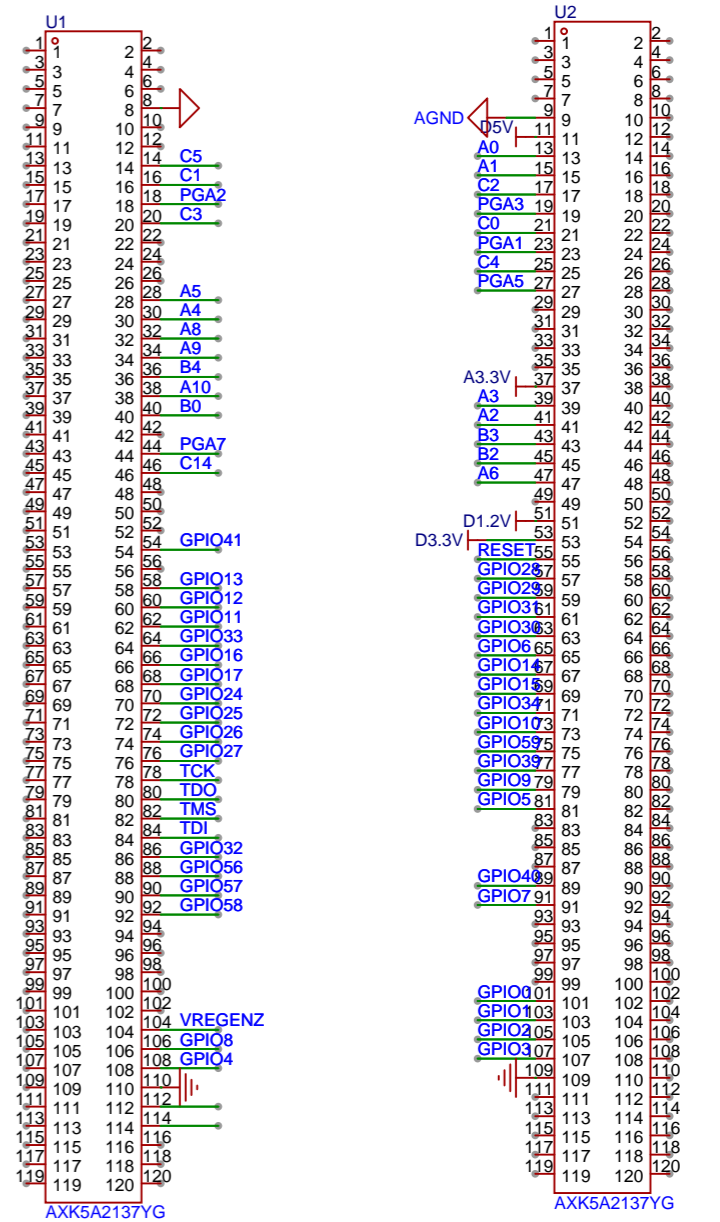
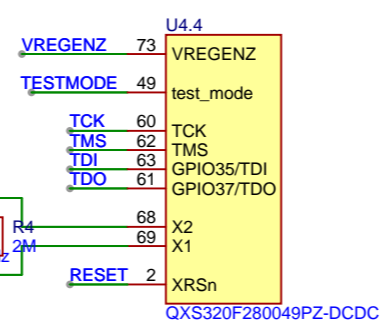


如果使用板载电压做VDAC基准电压，电容C22必须焊接且需短接开发板U7的31脚与核心板3.3V

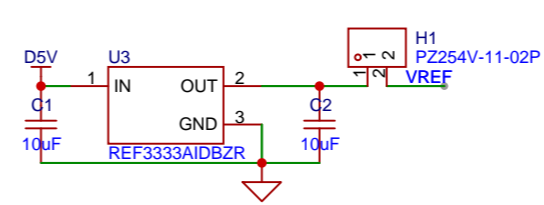
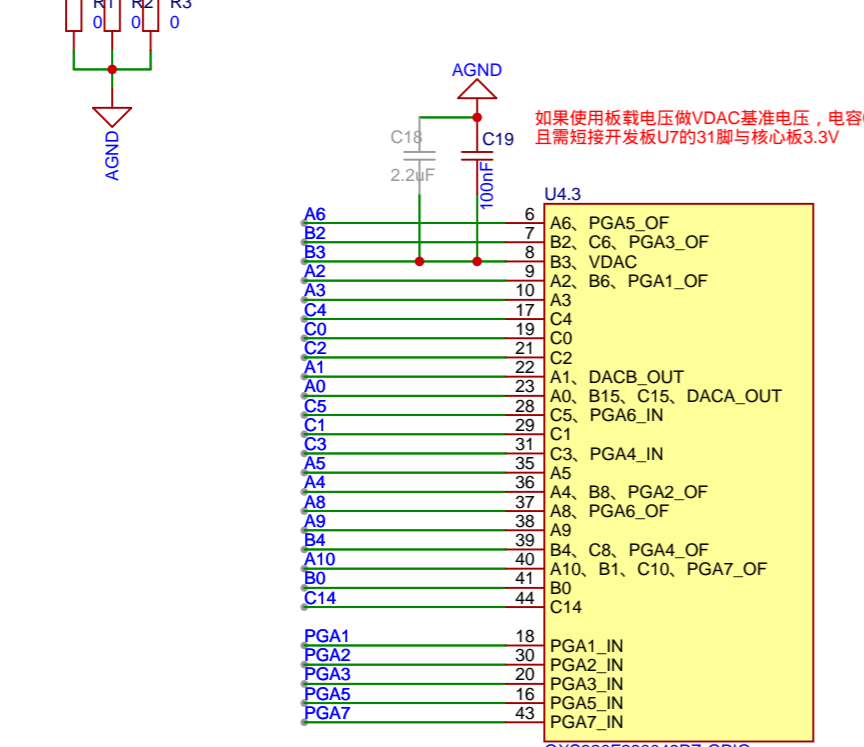
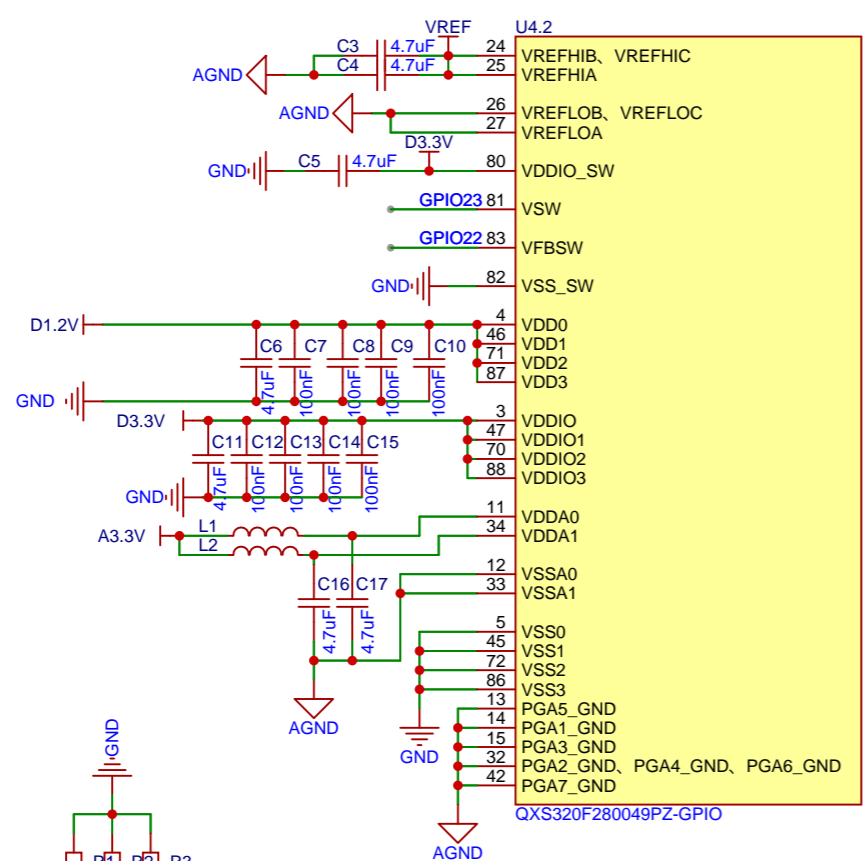


使用ADC外部基准参考电压，需短接H1端子1脚与2脚

U4.1		
GPIO0	EPWM1_A	79
GPIO1	EPWM1_B	78
GPIO2	EPWM2_A	77
GPIO3	EPWM2_B	76
GPIO4	EPWM3_A	75
GPIO5	EPWM3_B	89
GPIO6	EPWM4_A/SPIB_SIMO	97
GPIO7	EPWM4_B/SPIB_SIMO	84
GPIO8	EPWM5_A	74
GPIO9	EPWM5_B	90
GPIO10	EPWM6_A	93
GPIO11	EPWM6_B	52
GPIO12	EPWM7_A	51
GPIO13	EPWM7_B	50
GPIO14	EPWM8_A	96
GPIO15	EPWM8_B	95
GPIO16	SPIA_SIMO	54
GPIO17		55
GPIO24	SCIA_TX	56
GPIO25	SCIA_RX	57
GPIO26	I2C_SDA	58
GPIO27	I2C_SCL	59
GPIO28	SPIB_CLK	1
GPIO29	SPIB_STE	100
GPIO30	CAN_RX	98
GPIO31	CAN_TX	99
GPIO32		64
GPIO33		53
GPIO34		94
GPIO39		91
GPIO40		85
GPIO41	SPIA_CLK	48
GPIO56	SCIB_TX	65
GPIO57	SCIB_RX	66
GPIO58	SPIB_CLK	67
GPIO59		92
GPIO0		79
GPIO1		78
GPIO2		77
GPIO3		76
GPIO4		75
GPIO5		89
GPIO6		97
GPIO7		84
GPIO8		74
GPIO9		90
GPIO10		93
GPIO11		52
GPIO12		51
GPIO13		50
GPIO14		96
GPIO15		95
GPIO16		54
GPIO17		55
GPIO24		56
GPIO25		57
GPIO26		58
GPIO27		59
GPIO28		1
GPIO29		100
GPIO30		98
GPIO31		99
GPIO32		64
GPIO33		53
GPIO34		94
GPIO39		91
GPIO40		85
GPIO41		48
GPIO56		65
GPIO57		66
GPIO58		67
GPIO59		92

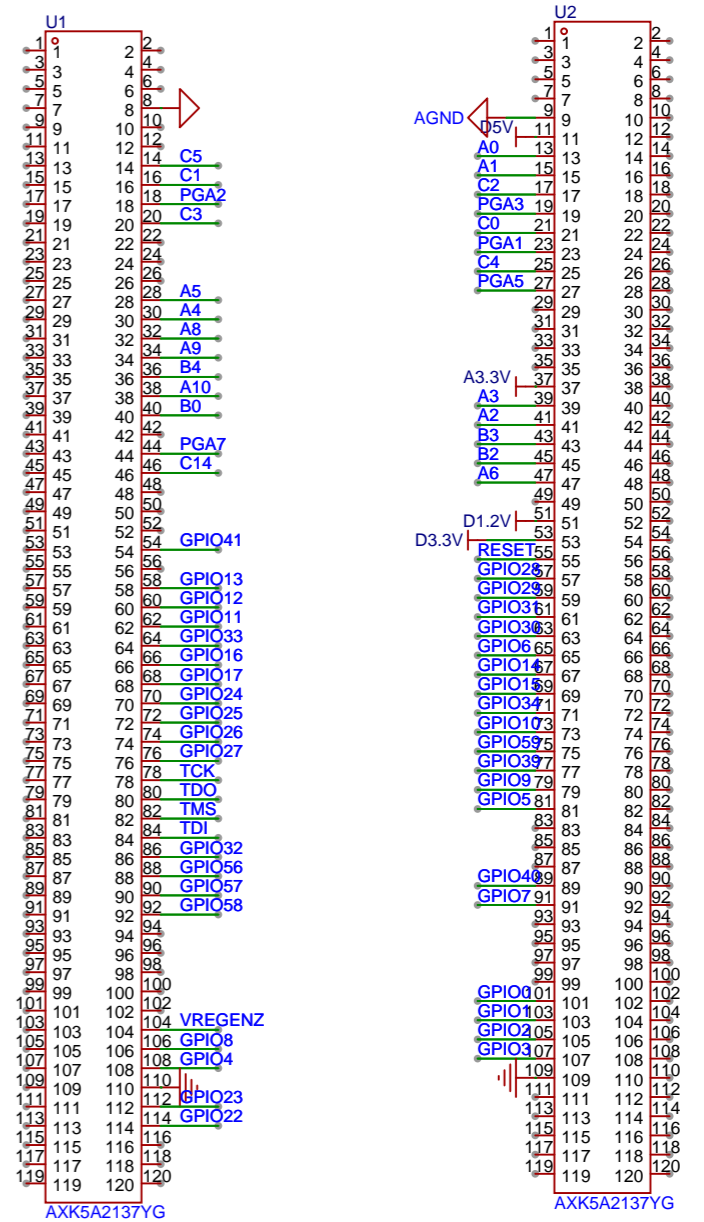
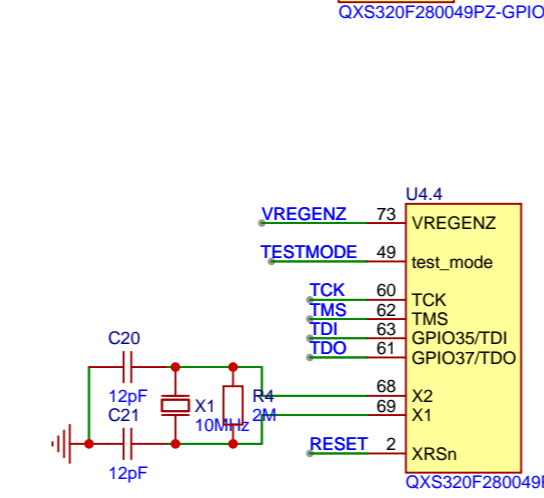


原理图	049PZ-100PIN-BA	创建日期	2025-04-21
		更新日期	2025-06-24
板子	049PZ-100PIN-BA	图页	P1
绘制	QXS320F280049开发评估板(0515)		
审阅			
版本		尺寸	页 1 共 1
嘉立创EDA		V1.0	A4
嘉立创EDA			



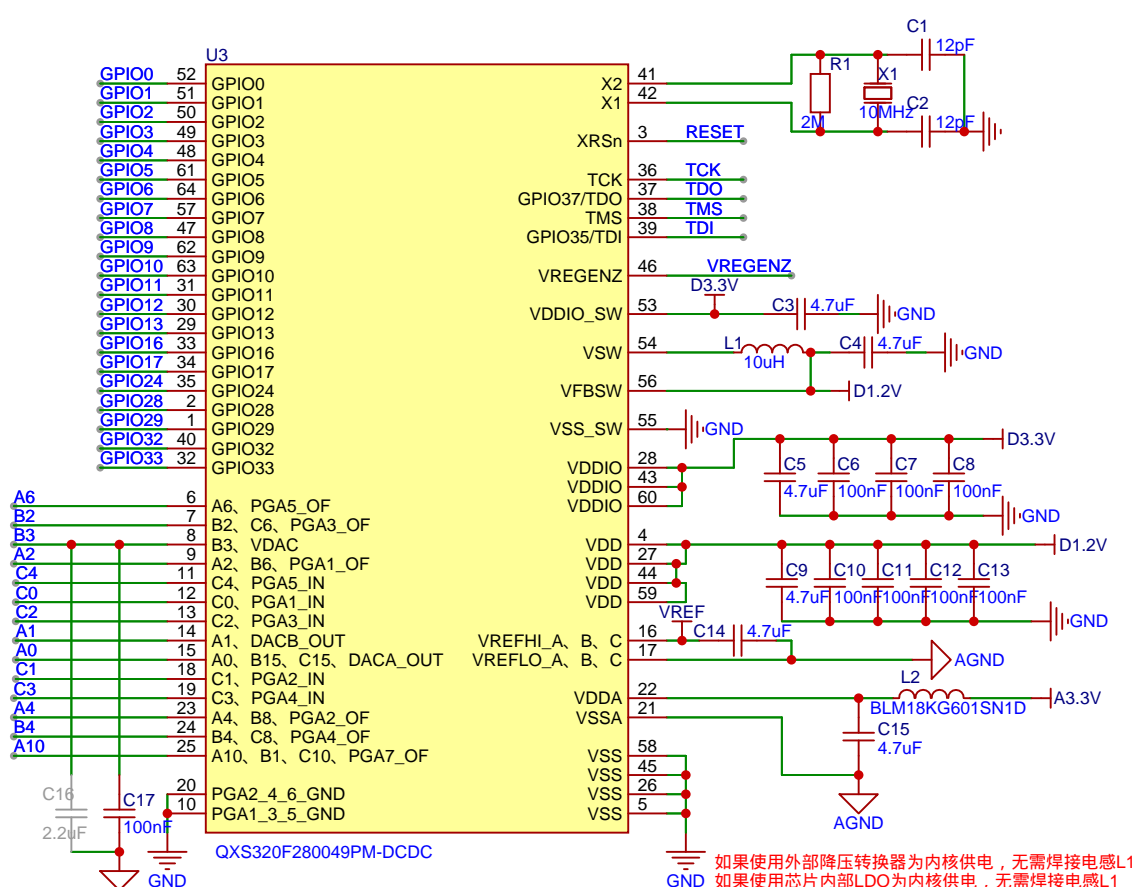
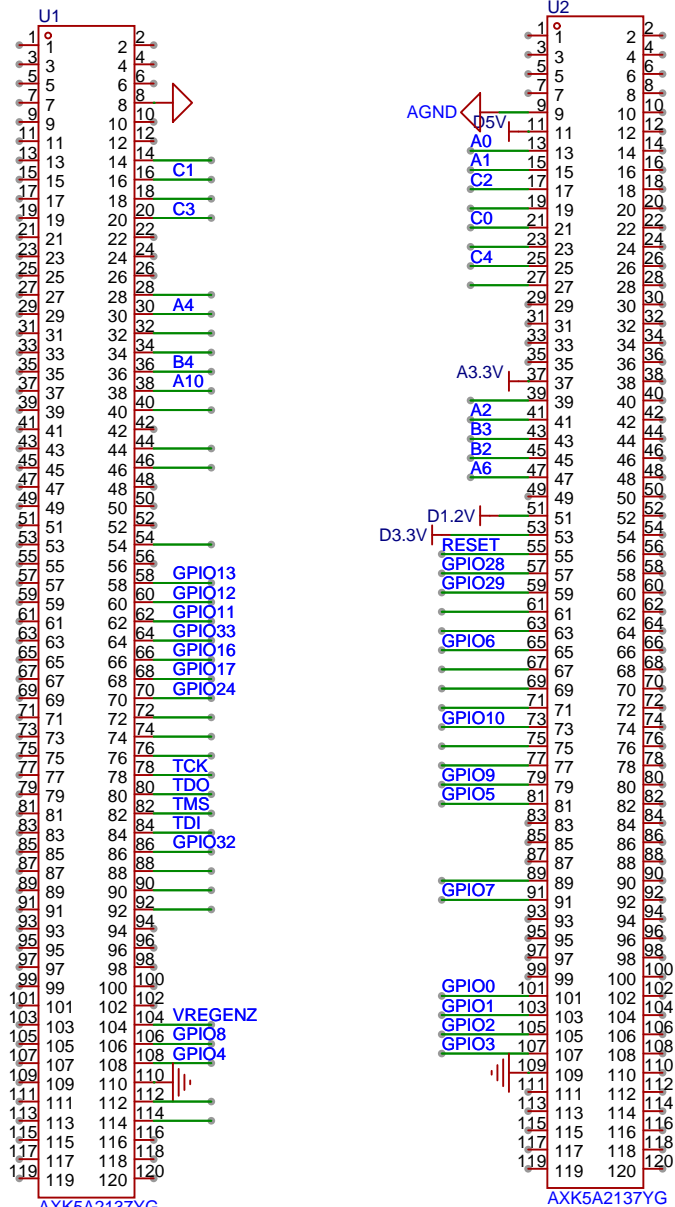
使用ADC外部基准参考电压，需短接H1端子1脚与2脚

U4.1		
GPIO0	EPWM1_A	79
GPIO1	EPWM1_B	78
GPIO2	EPWM2_A	77
GPIO3	EPWM2_B	76
GPIO4	EPWM3_A	75
GPIO5	EPWM3_B	89
GPIO6	EPWM4_A/SPIB_SIMO	97
GPIO7	EPWM4_B/SPIB_SIMO	84
GPIO8	EPWM5_A	74
GPIO9	EPWM5_B	90
GPIO10	EPWM6_A	93
GPIO11	EPWM6_B	52
GPIO12	EPWM7_A	51
GPIO13	EPWM7_B	50
GPIO14	EPWM8_A	96
GPIO15	EPWM8_B	95
GPIO16	SPIA_SIMO	54
GPIO17		55
GPIO24	SCIA_TX	56
GPIO25	SCIA_RX	57
GPIO26	I2C_SDA	58
GPIO27	I2C_SCL	59
GPIO28	SPIB_CLK	1
GPIO29	SPIB_STE	100
GPIO30	CAN_RX	98
GPIO31	CAN_TX	99
GPIO32		64
GPIO33		53
GPIO34		94
GPIO39		91
GPIO40		85
GPIO41	SPIA_CLK	48
GPIO56	SCIB_TX	65
GPIO57	SCIB_RX	66
GPIO58	SPIB_CLK	67
GPIO59		92



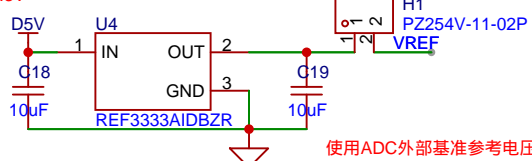
原理图	049PZ-100PIN-BB	创建日期	2025-05-14
板子	049PZ-100PIN-BB	更新日期	2025-06-24
绘制		图页	P1
审阅		QXS320F280049开发评估板(0515)	
	版本	尺寸	页 1 共 1
	V1.0	A4	嘉立创EDA





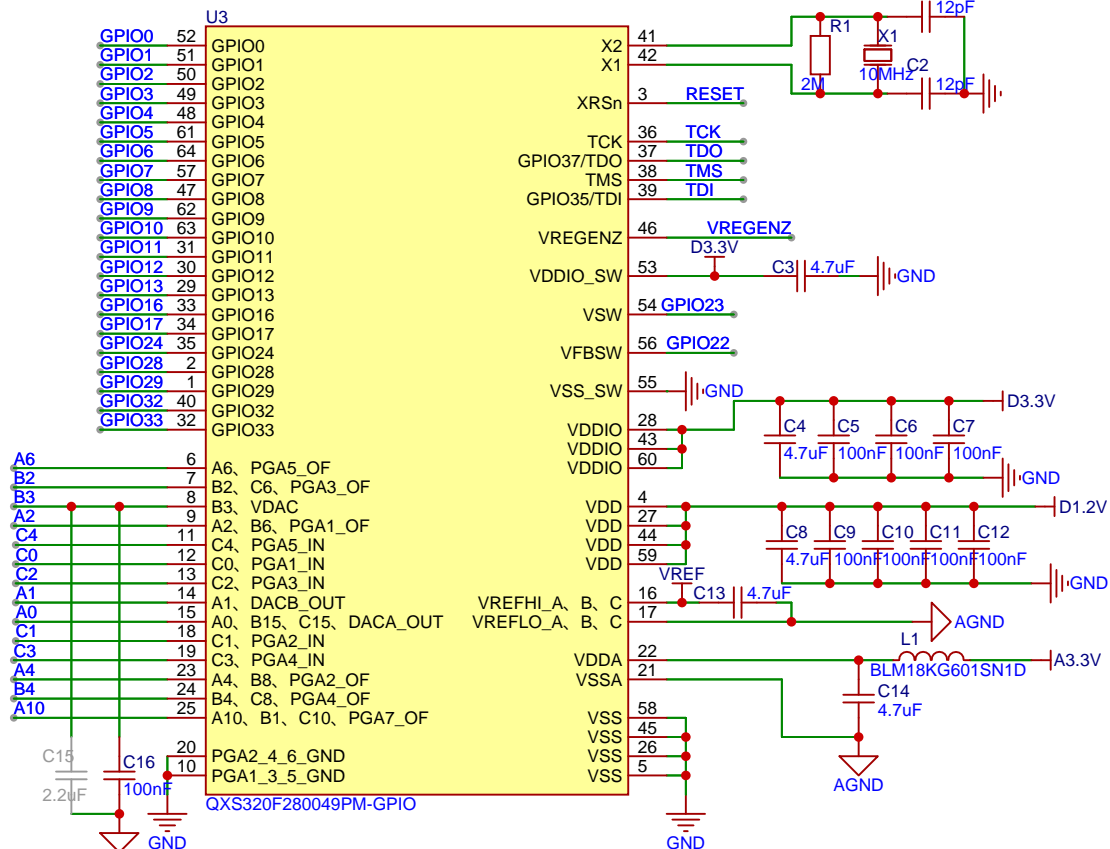
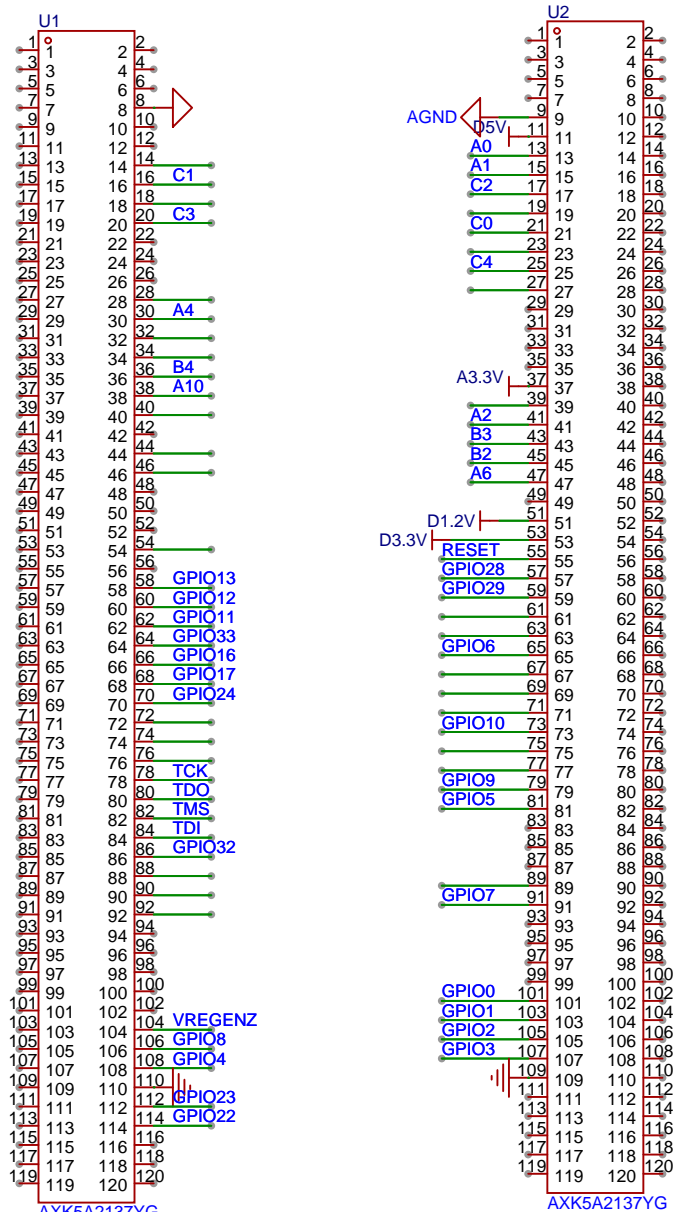
如果使用板载电压做VDAC基准电压，电容C22必须焊接且需短接开发板U7的31脚与核心板3.3V

如果使用外部降压转换器为内核供电，无需焊接电感L1
 如果使用芯片内部LDO为内核供电，无需焊接电感L1
 如果使用芯片内部DCDC为内核供电，电感L1必须焊接

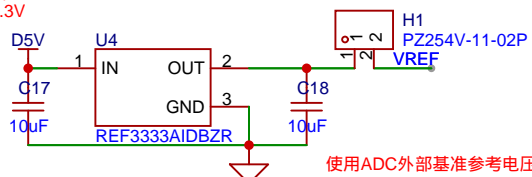


使用ADC外部基准参考电压，需短接H1端子1脚与2脚

原理图	049PM-64PIN-BA	创建日期	2025-04-21
		更新日期	2025-06-24
板子	049PM-64PIN-BA	图页	P1
绘制	QXS320F280049开发评估板 (0515)		
审阅			
		版本	尺寸
		V1.0	A4
		页 1 共 1	
嘉立创EDA		嘉立创EDA	

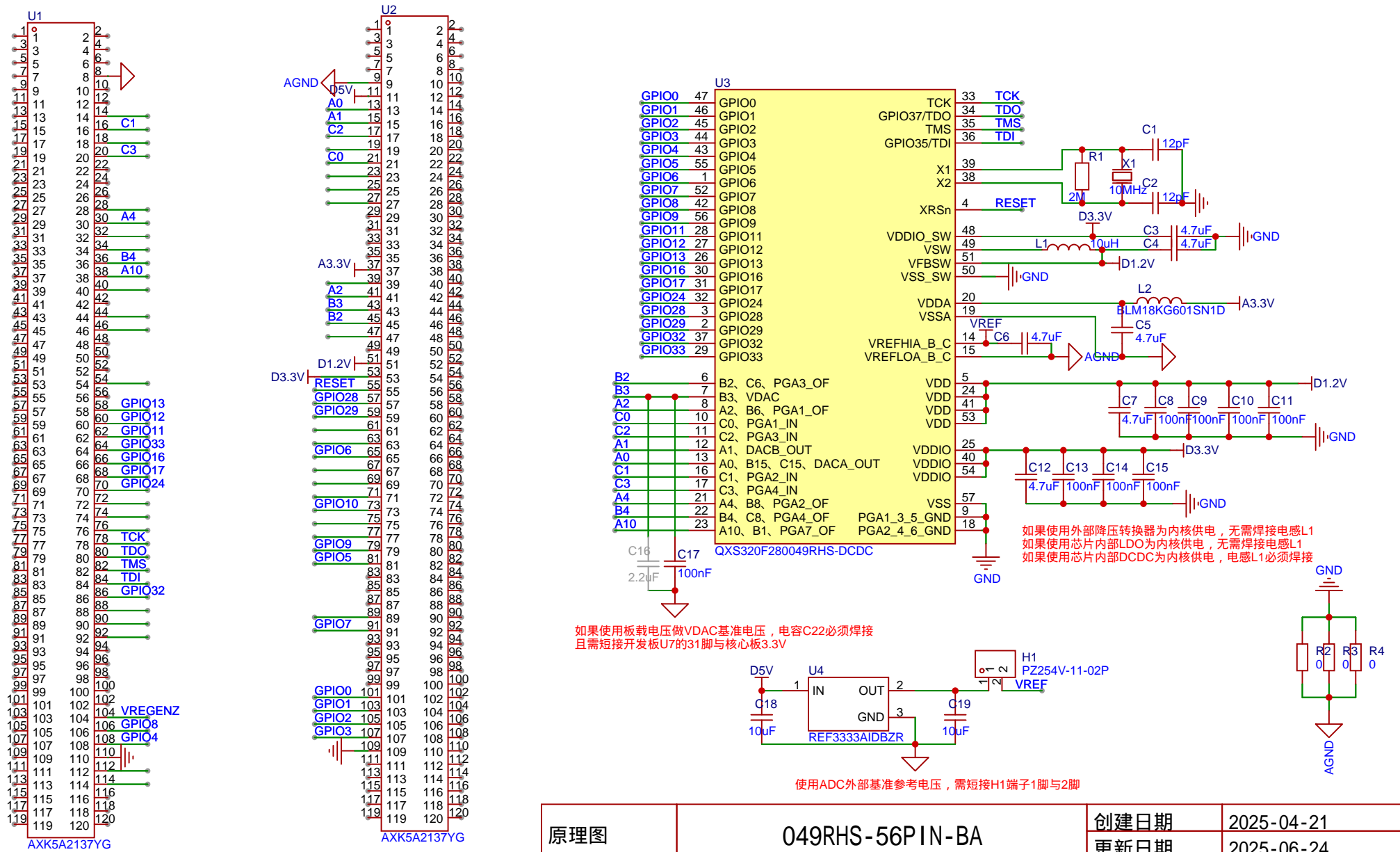


如果使用板载电压做VDAC基准电压，电容C22必须焊接且需短接开发板U7的31脚与核心板3.3V

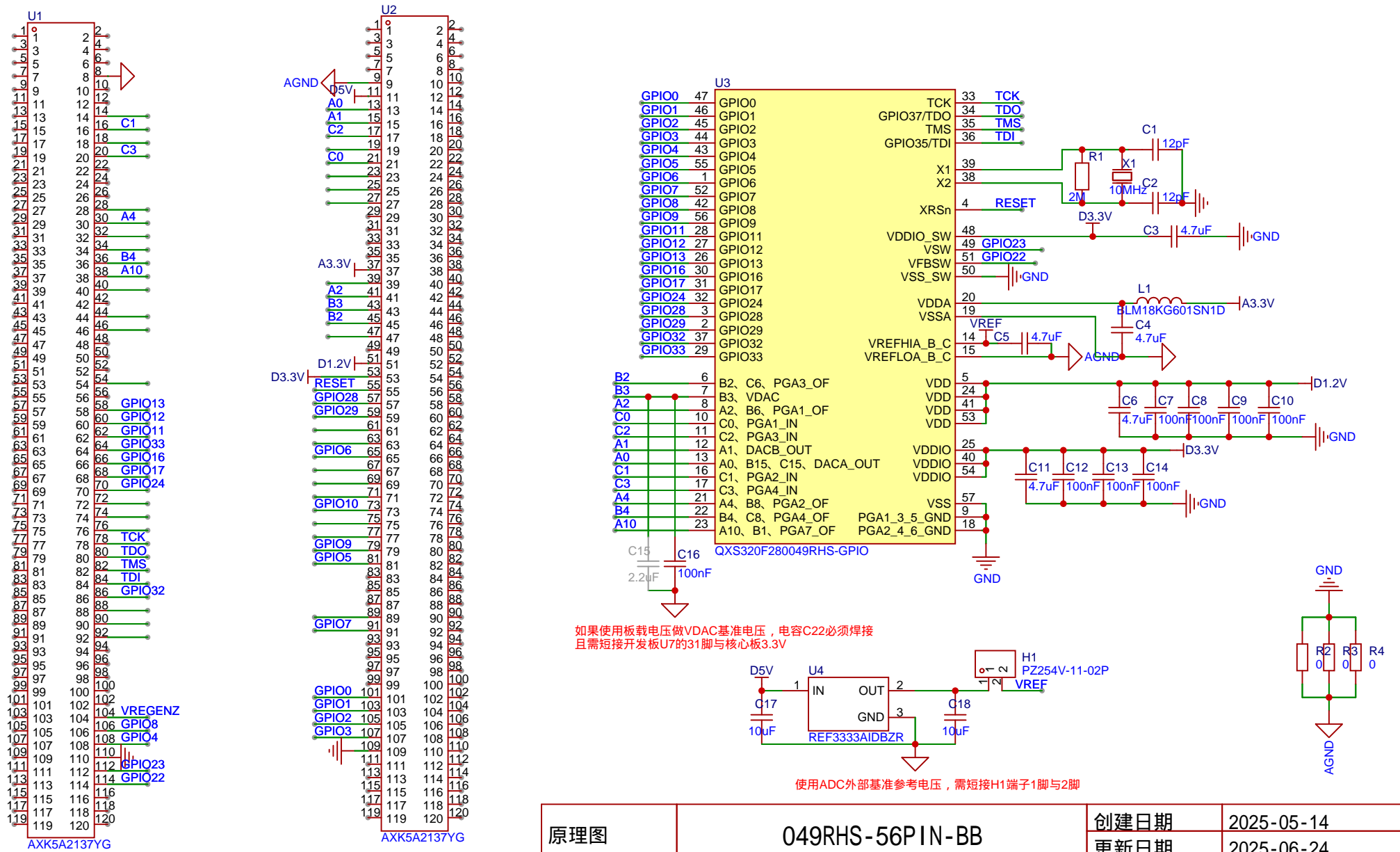


使用ADC外部基准参考电压，需短接H1端子1脚与2脚

原理图	049PM-64PIN-BB	创建日期	2025-05-14
		更新日期	2025-06-24
板子	049PM-64PIN-BB	图页	P1
绘制	QXS320F280049开发评估板 (0515)		
审阅			
		版本	尺寸
		V1.0	A4
		页 1 共 1	
嘉立创EDA		嘉立创EDA	



原理图	049RHS-56PIN-BA	创建日期	2025-04-21
		更新日期	2025-06-24
板子	049RHS-56PIN-BA	图页	P1
绘制	QXS320F280049开发评估板(0515)		
审阅			
		版本	尺寸
		V1.0	A4
		页 1 共 1	
嘉立创EDA		嘉立创EDA	



原理图	049RHS-56PIN-BB	创建日期	2025-05-14	
		更新日期	2025-06-24	
板子	049RHS-56PIN-BB	图页	P1	
绘制	QXS320F280049开发评估板(0515)			
审阅				
		版本	尺寸	页 1 共 1
嘉立创EDA		V1.0	A4	嘉立创EDA