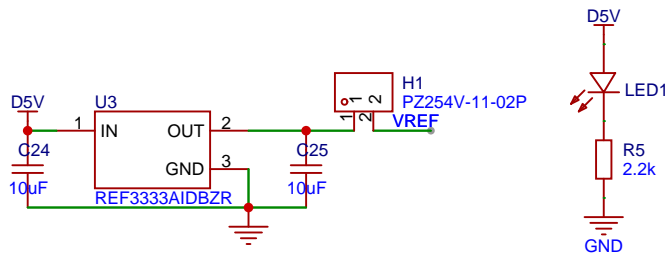
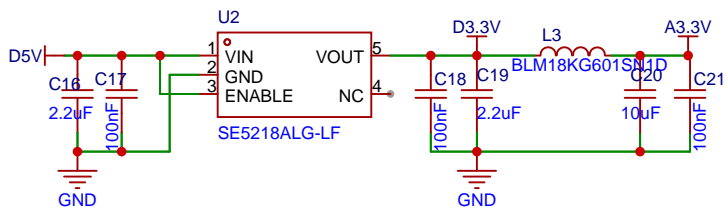
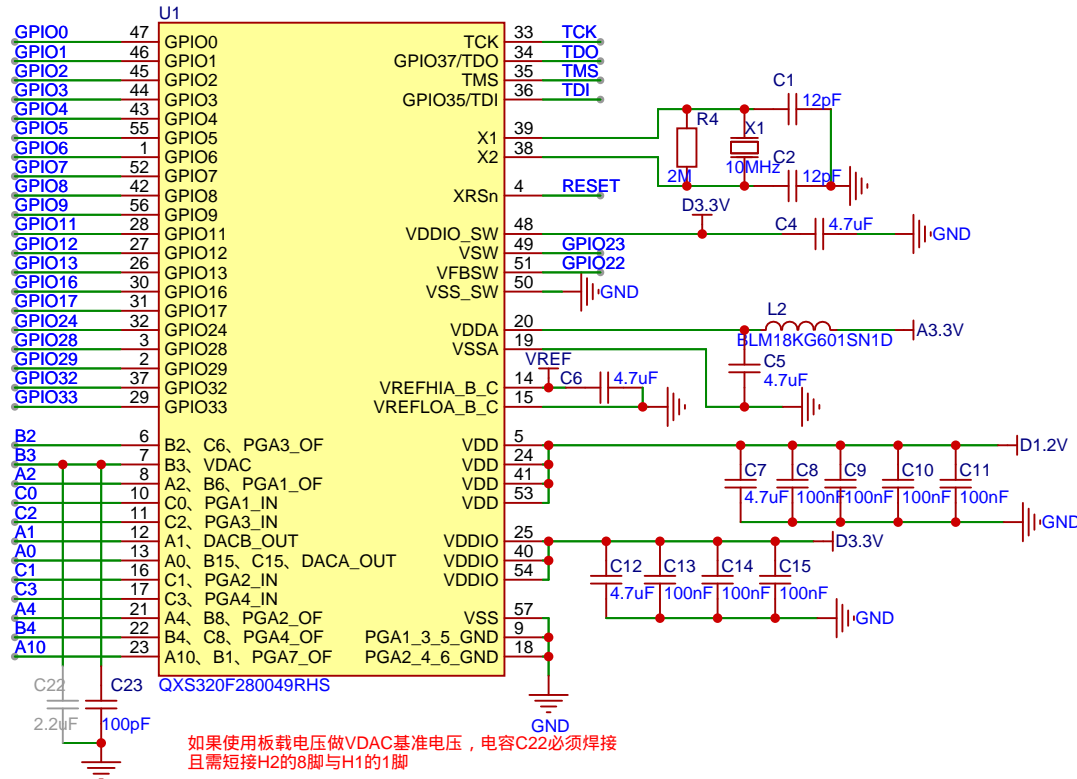


电源

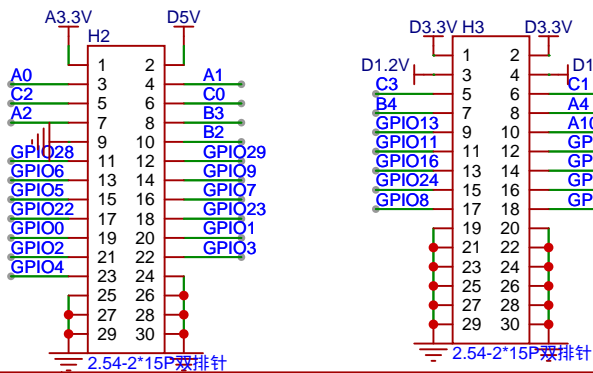
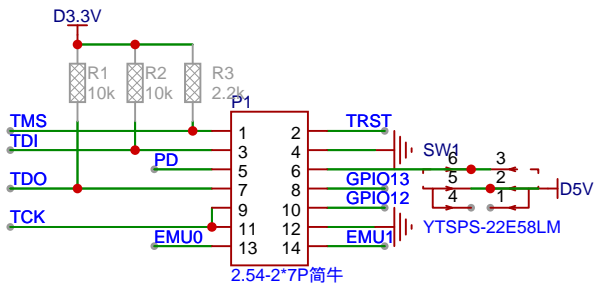


使用ADC外部基准参考电压，需短接H2端子1脚与2脚

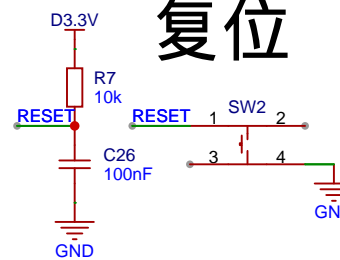


如果使用板载电压做VDAC基准电压，电容C22必须焊接且需短接H2的8脚与H1的1脚

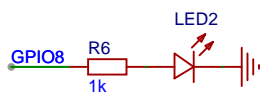
JTAG



复位



LED



原理图

049-56PIN-GPIO

创建日期

2025-09-16

更新日期

2026-03-06

板子

049-56PIN-GPIO

图页

P1

QXS320F280049最小系统板V1.0 - 14pin

版本

尺寸

页 1 共 1

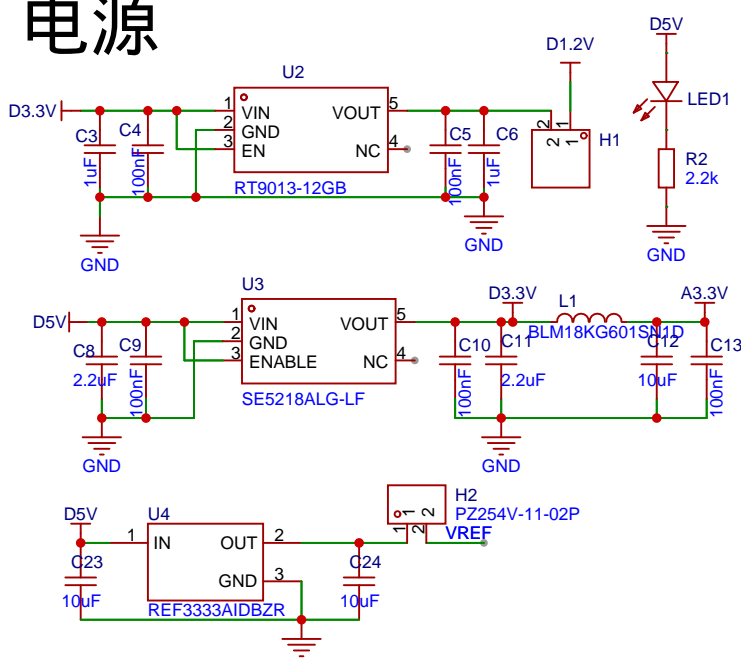
嘉立创EDA

V1.0

A4

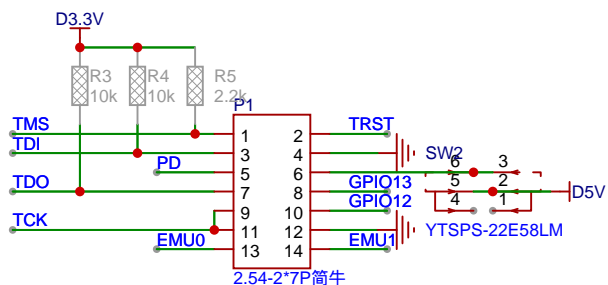
嘉立创EDA

电源



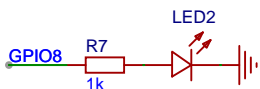
使用ADC外部基准参考电压，需短接H2端子1脚与2脚

JTAG

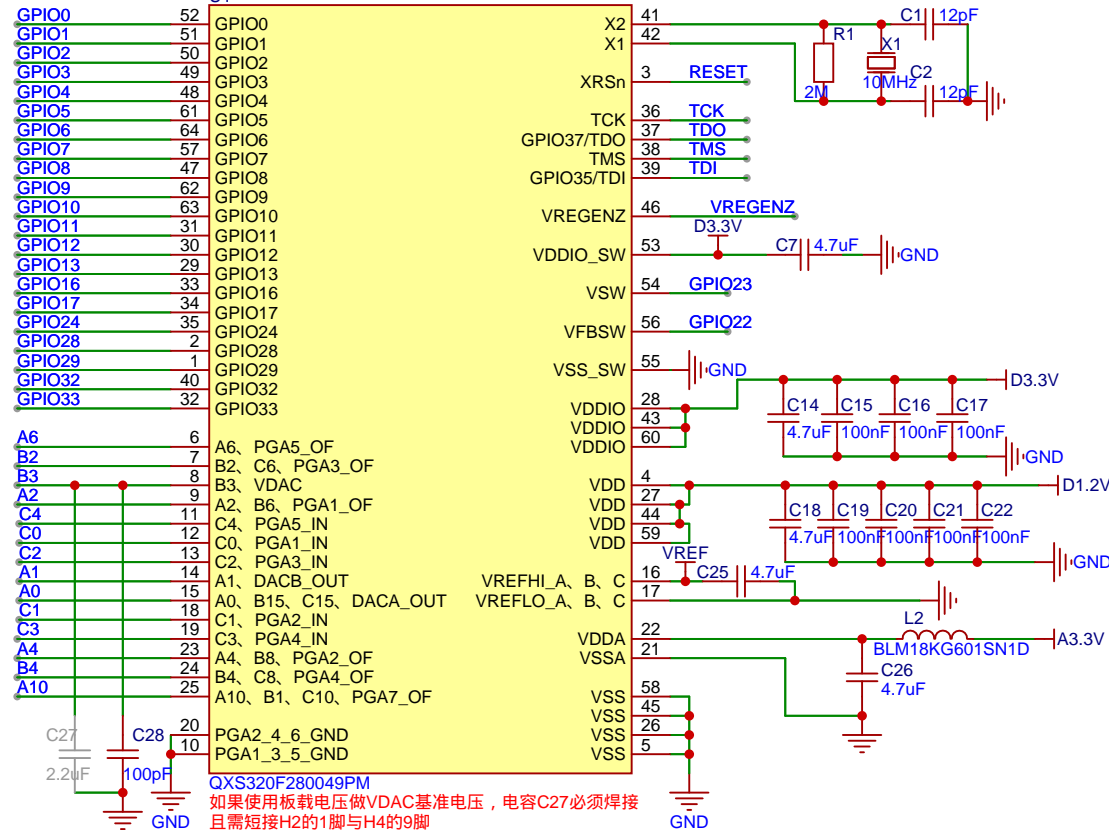
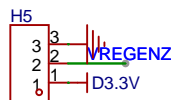


2.54-2*7P简牛

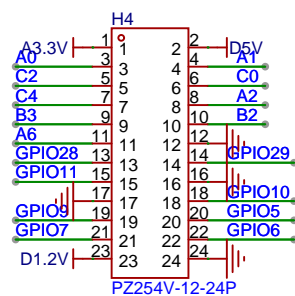
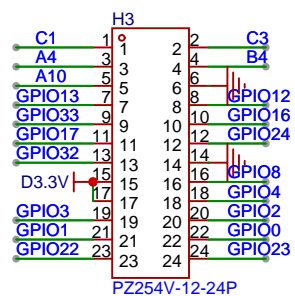
LED



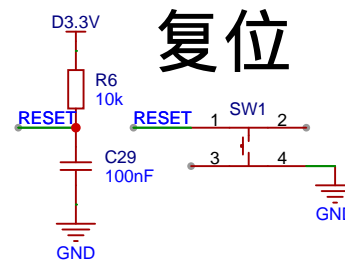
供电选择



如果使用板载电压做VDAC基准电压，电容C27必须焊接且需短接H2的1脚与H4的9脚

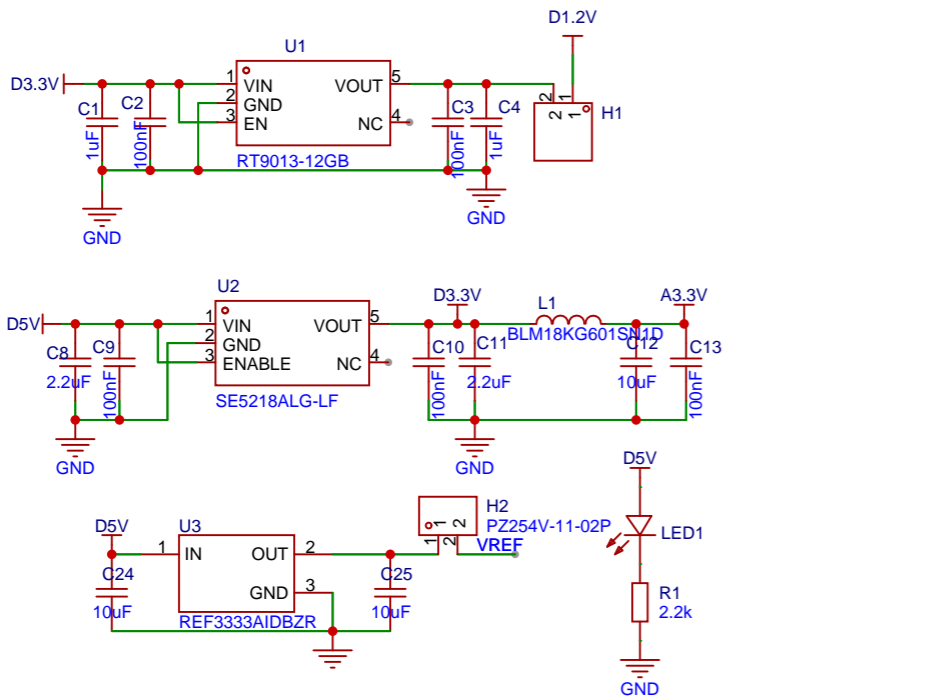


复位

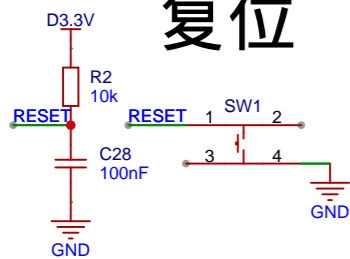


原理图	049-64PIN-GPIO		创建日期	2025-09-16
板子	049-64PIN-GPIO		更新日期	2026-03-06
绘制			图页	P1
审阅			QXS320F280049最小系统板V1.0 - 14pin	
			版本	V1.0
			尺寸	A4
			页	1 共 1

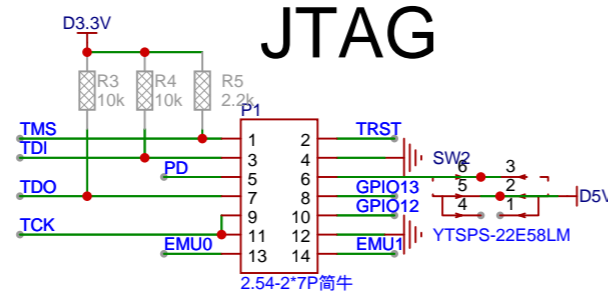
电源



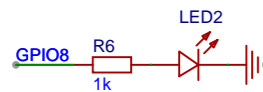
复位



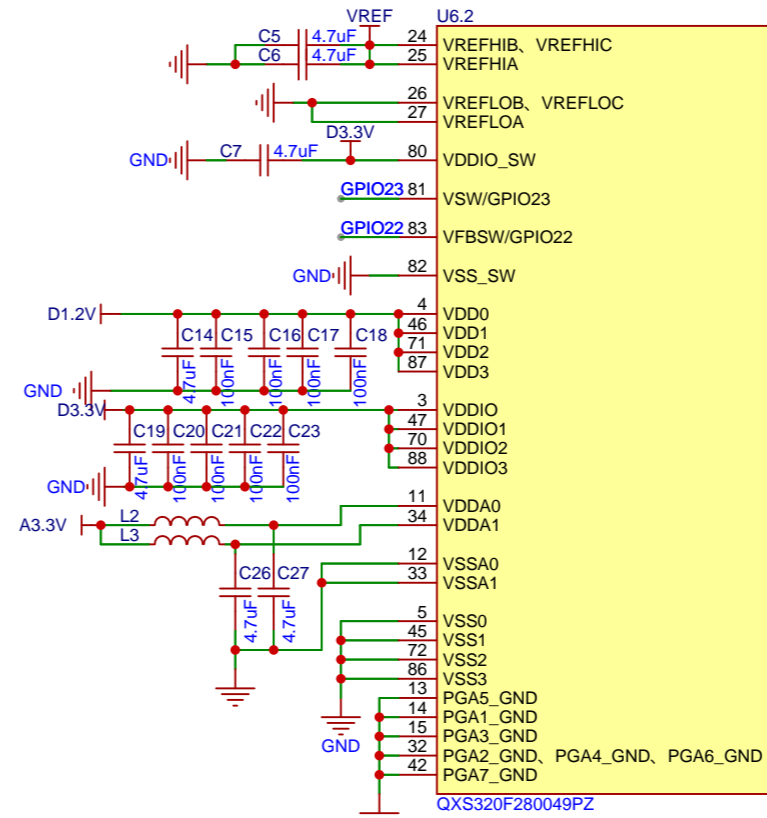
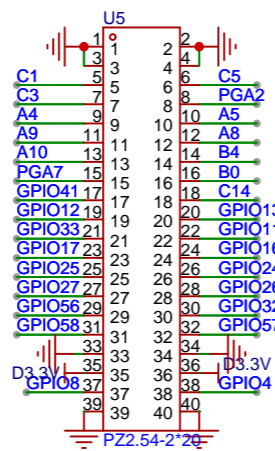
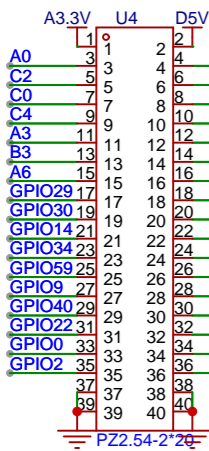
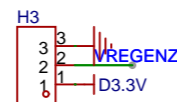
JTAG



LED



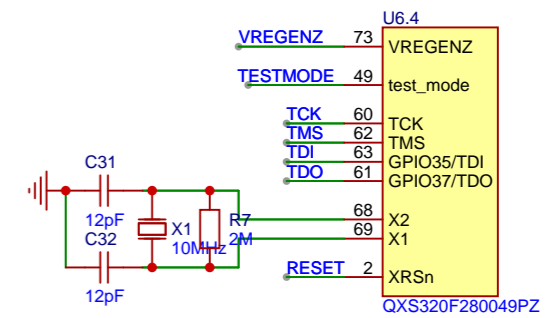
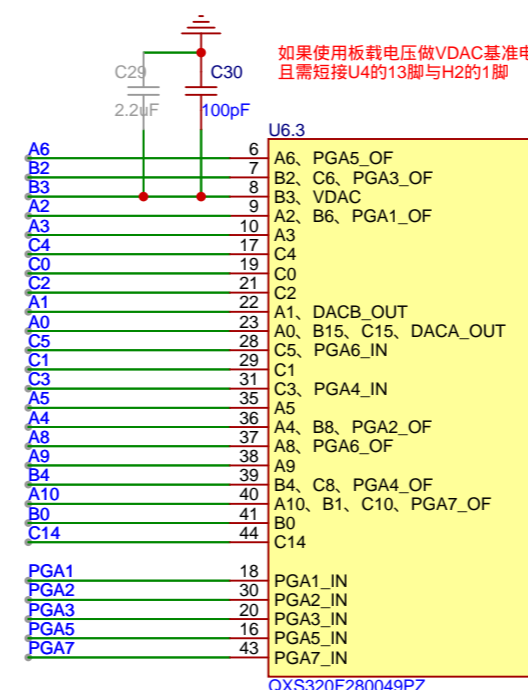
供电选择



U6.1	
GPIO0	79
GPIO1	78
GPIO2	77
GPIO3	76
GPIO4	75
GPIO5	89
GPIO6	97
GPIO7	84
GPIO8	74
GPIO9	90
GPIO10	93
GPIO11	52
GPIO12	51
GPIO13	50
GPIO14	96
GPIO15	95
GPIO16	54
GPIO17	55
GPIO24	56
GPIO25	57
GPIO26	58
GPIO27	59
GPIO28	1
GPIO29	100
GPIO30	98
GPIO31	99
GPIO32	64
GPIO33	53
GPIO34	94
GPIO39	91
GPIO40	85
GPIO41	48
GPIO56	65
GPIO57	66
GPIO58	67
GPIO59	92

QXS320F280049PZ

如果使用板载电压做VDAC基准电压，电容C29必须焊接且需短接U4的13脚与H2的1脚



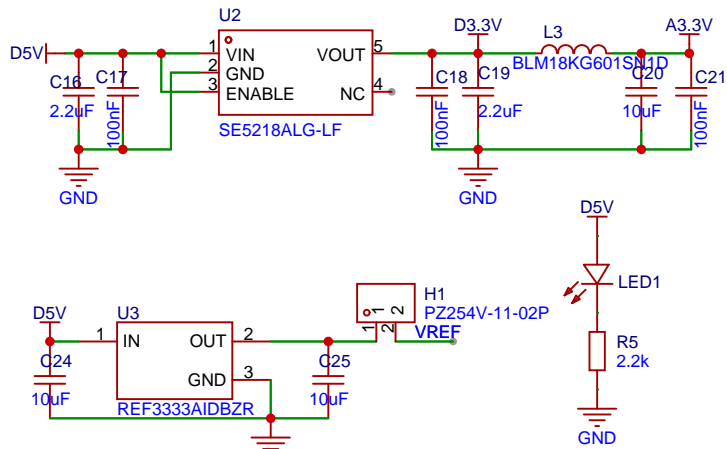
原理图	049-100PIN-GPIO	创建日期	2025-07-21
板子	049-100PIN-GPIO	更新日期	2026-03-06
绘制		图页	P1
审阅		QXS320F280049最小系统板V1.0 - 14pin	
	版本	尺寸	页 1 共 1
嘉立创EDA		V1.0	A4
嘉立创EDA			

QXS320F280049芯片针对GPIO22与GPIO23复用内部DCDC功能通过两种封装进行区分，不能通过切换内部寄存器进行复用切换

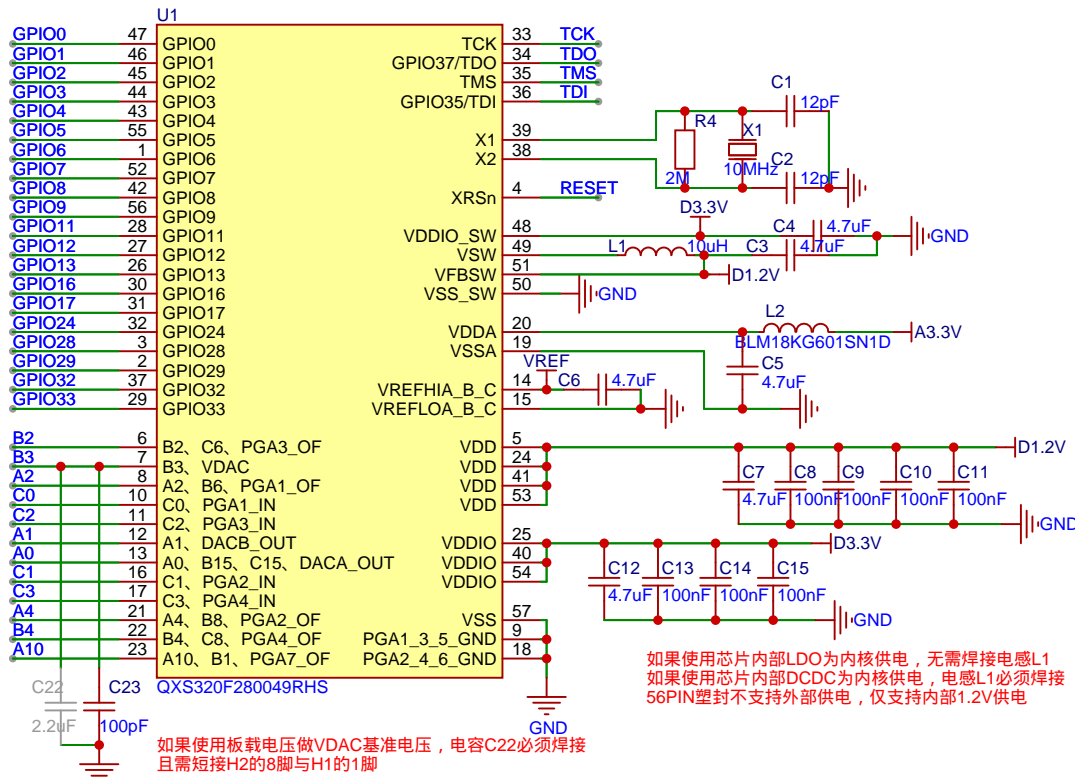
GPIO版本即芯片丝印AAABB尾缀，对应功能为GPIO功能

GPIO版本即芯片丝印AAABB尾缀，对应功能为GPIO功能

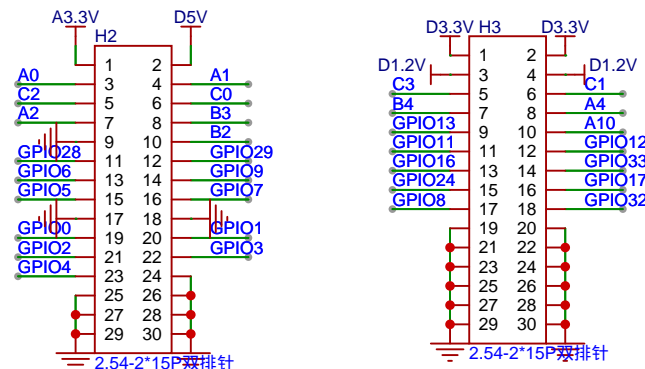
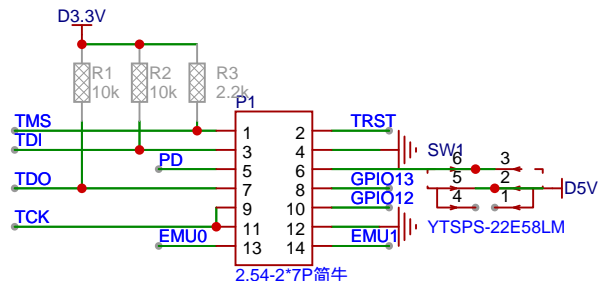
电源



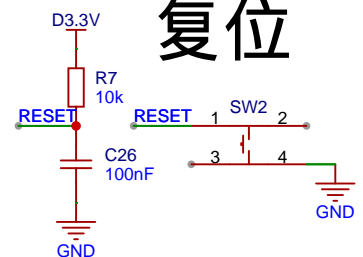
使用ADC外部基准参考电压，需短接H2端子1脚与2脚



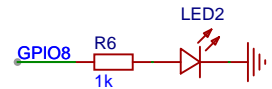
JTAG



复位

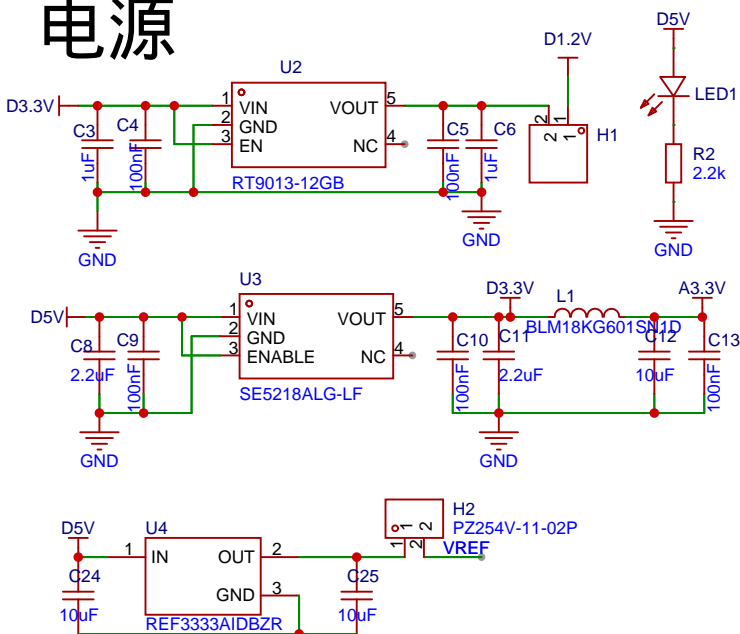


LED

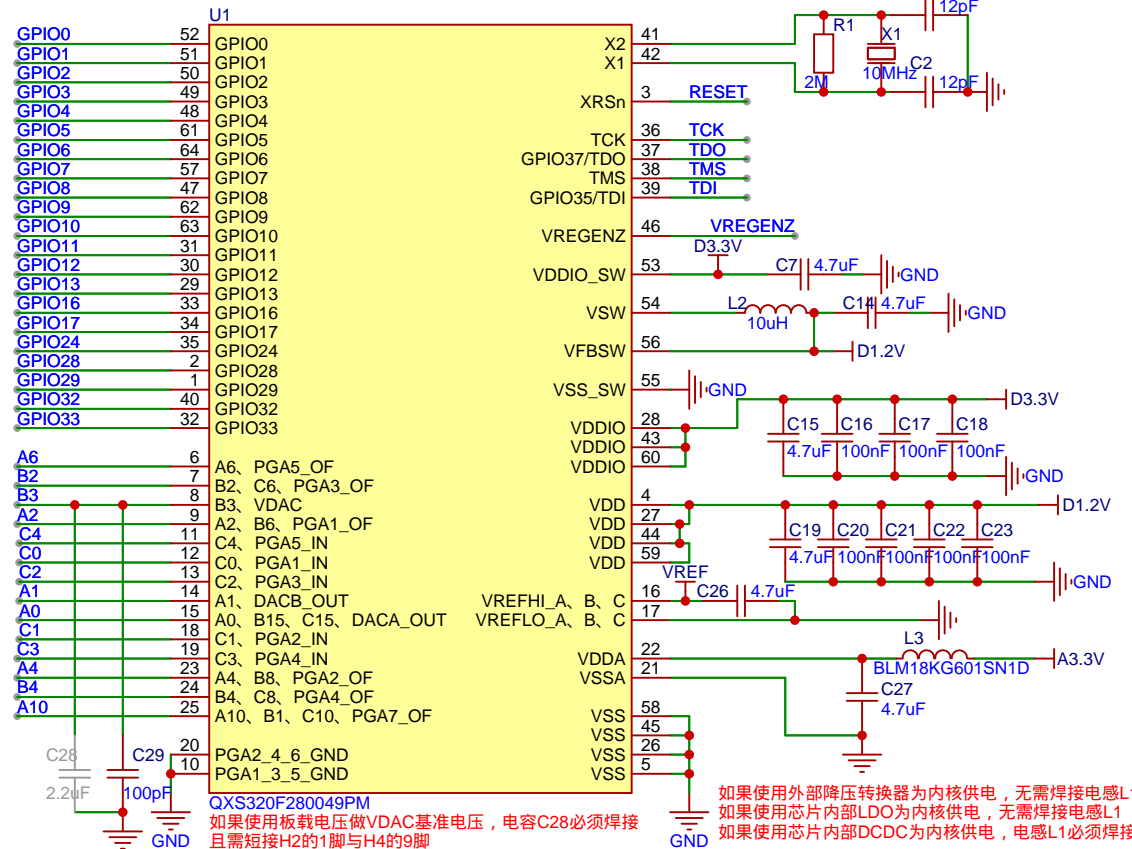


原理图	049-56PIN-DCDC		创建日期	2025-05-27
板子	049-56PIN-DCDC		更新日期	2026-03-06
绘制			图页	P1
审阅			QXS320F280049最小系统板V1.0 - 14pin	
			版本	尺寸
			V1.0	A4
			页	1 共 1
			嘉立创EDA	

电源

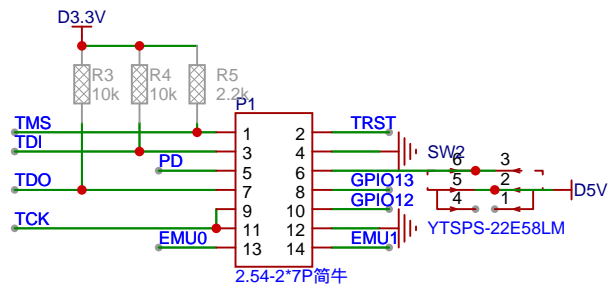


使用ADC外部基准参考电压，需短接H2端子1脚与2脚

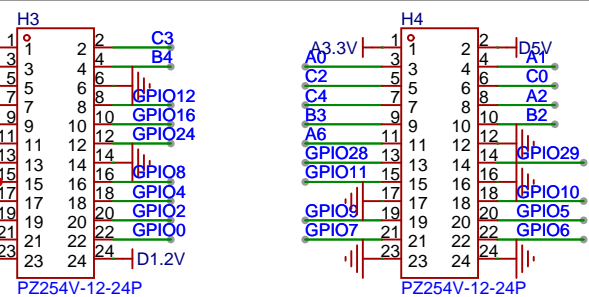


如果使用外部降压转换器为内核供电，无需焊接电感L1
 如果使用芯片内部LDO为内核供电，无需焊接电感L1
 如果使用芯片内部DCDC为内核供电，电感L1必须焊接

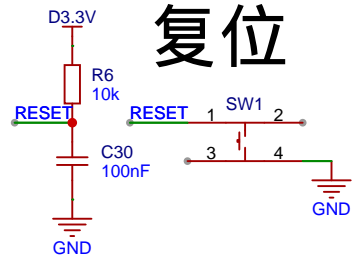
JTAG



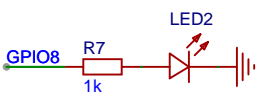
2.54-2*7P简牛



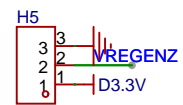
复位



LED



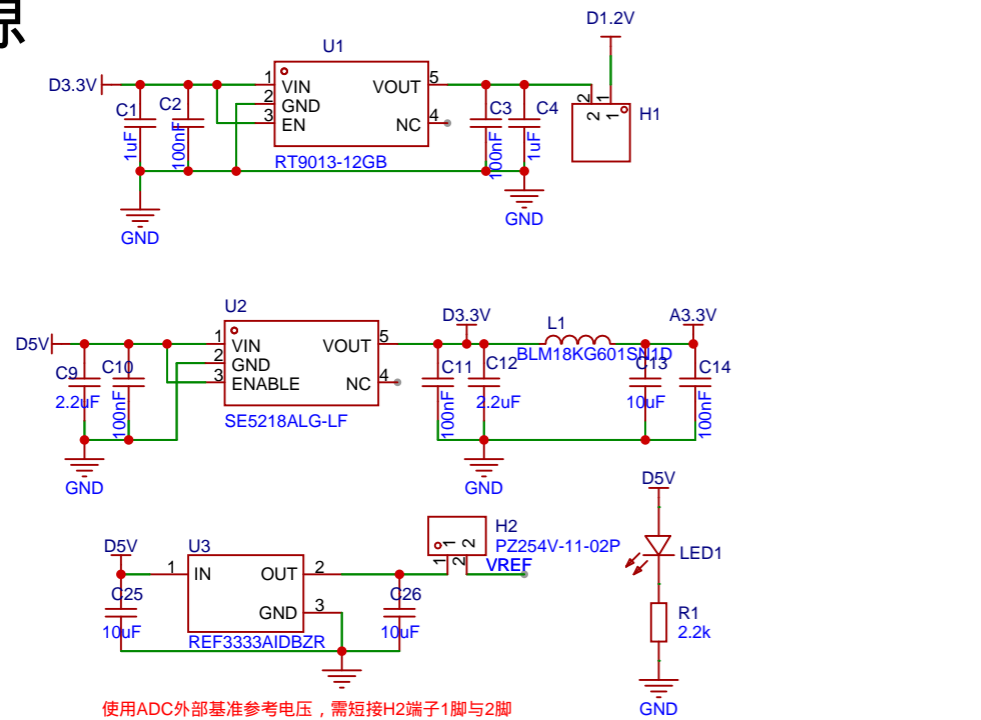
供电选择



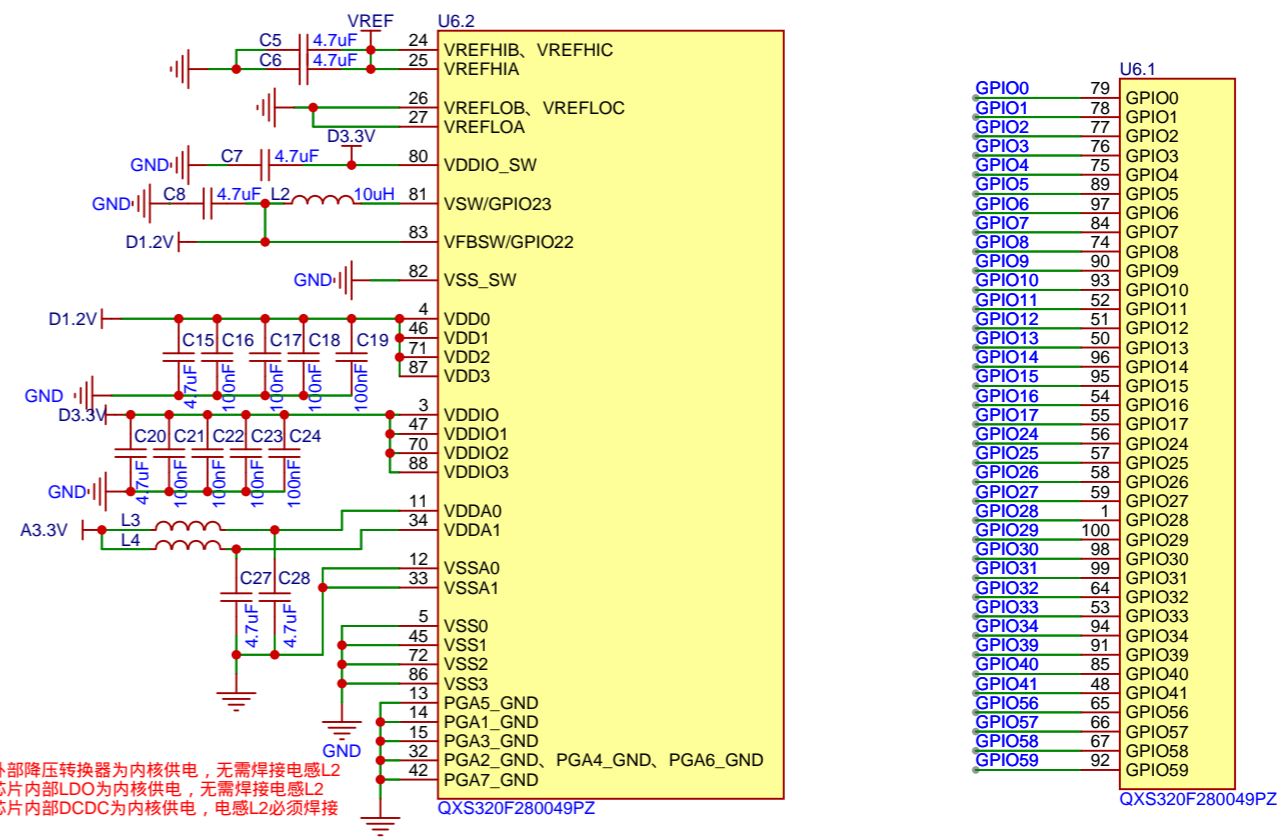
原理图	049-64PIN-DCDC	创建日期	2025-05-21
板子	049-64PIN-DCDC	更新日期	2026-03-06
绘制		图页	P1
审阅		QXS320F280049最小系统板V1.0 - 14pin	
		版本	尺寸
		V1.0	A4
		页	1 共 1
		嘉立创EDA	



电源

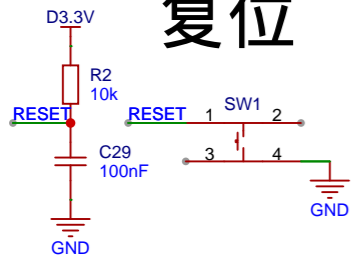


使用ADC外部基准参考电压，需短接H2端子1脚与2脚

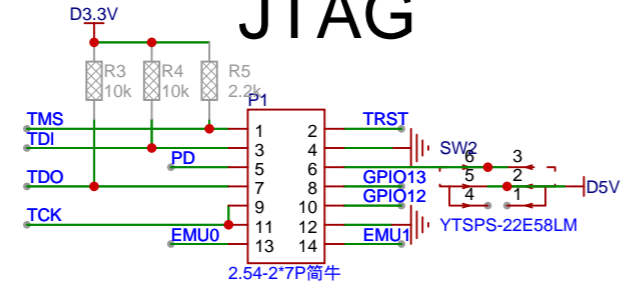


如果使用外部降压转换器为内核供电，无需焊接电感L2
 如果使用芯片内部DCDC为内核供电，无需焊接电感L2
 如果使用芯片内部DCDC为内核供电，电感L2必须焊接

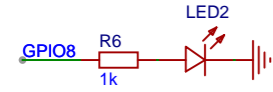
复位



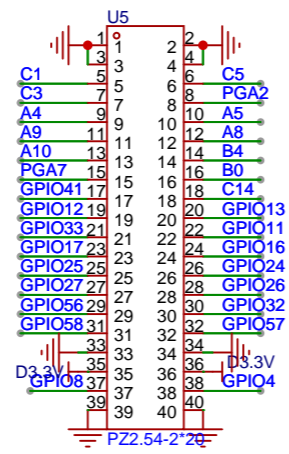
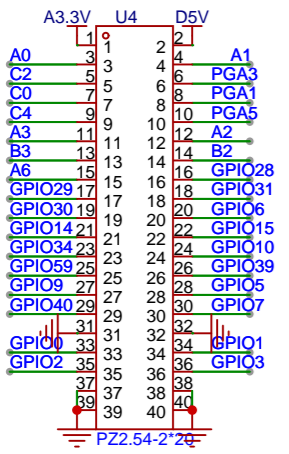
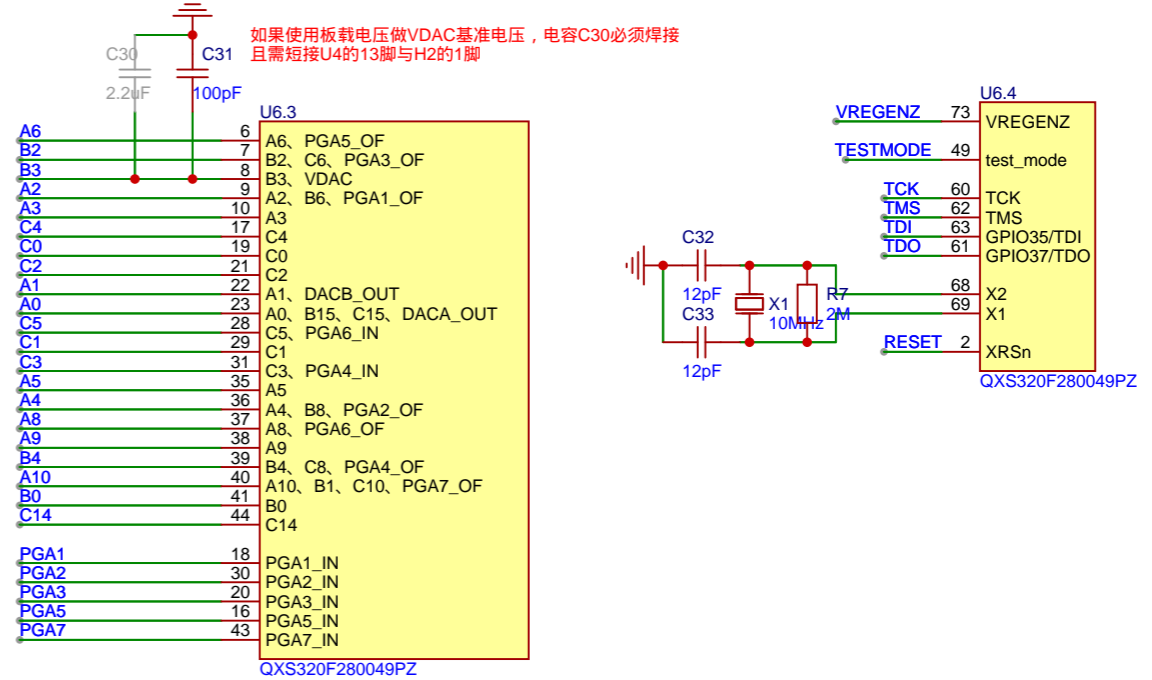
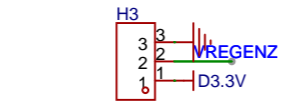
JTAG



LED



供电选择



原理图	049-100PIN-DCDC	创建日期	2025-09-16
板子	049-100PIN-DCDC	更新日期	2026-03-06
绘制	QXS320F280049最小系统板V1.0 - 14pin		
审阅			
		版本	尺寸
		V1.0	A4
		页 1 共 1	
嘉立创EDA		嘉立创EDA	